

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application:) For: CACHED MEMORY SYSTEM
Sih, et al.) AND CACHE CONTROLLER
) FOR EMBEDDED DIGITAL
Application No.: 10/807,648) SIGNAL PROCESSOR
)
Filed: March 24, 2004) Examiner: Song, Jasmine
)
Confirmation No.: 3901) Group No.: 2188
)
) Docket No.: 030459

Mailstop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY UNDER 37 C.F.R. § 1.116

Sir:

In response to the Final Office Action of May 2, 2008, please consider the following:

A Listing of the Claims are reflected in the listing of claims which begins on page 2 of this paper; and

Remarks/Arguments begin on page 9 of this paper.